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L1	2278	(341/156,155).CCLS.	USPAT	OR ·	OFF	2007/01/18 09:41
L2	437	(341/156,155).CCLS.	US-PGPU B	OR	OFF	2007/01/18 09:42
L3	52	(341/156).CCLS.	US-PGPU B	OR	OFF	2007/01/18 09:42
L4	660	(analog ADJ1 digital or ad or adc) image array ampl\$5	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	SAME	ON	2007/01/18 09:43
L5	100	(analog ADJ1 digital or ad or adc) image array ampl\$5	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	WITH	ON COST	2007/01/18 09:43
L6	1	(analog ADJ1 digital or ad or adc) image array ampl\$5 and l1	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	WITH	ON	2007/01/18 09:44

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L7	6	(analog ADJ1 digital or ad or adc) image array ampl\$5 and l1	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	SAME	ON	2007/01/18 ₋ 09:44
L8	0	(analog ADJ1 digital or ad or adc) image array ampl\$5 and I3	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	SAME	ON	2007/01/18 09:44
L9	3465	(analog ADJ1 digital or ad or adc) image array	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	SAME	ON	2007/01/18 09:44
L10	0	(analog ADJ1 digital or ad or adc) image array and I3	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	SAME	ON .	2007/01/18 09:44

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1 Track 6: autonomic and organic computing: Marching-pixels: a new

paradigm for smart sensor processor arrays

Dietmar Fey, Daniel Schmidt

May 2005 Proceedings of the 2nd conference on Computing front

Publisher: ACM Press

Full text available: pdf(606.57 Additional Information: full citation, KB) index terms

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mixed-signal applications

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Publisher: IEEE Computer Society

Full text available: pdf(75.54

KB) 🖲 Publisher

Additional Information: full citation,

index terms

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5 (Special session) presentation + poster disscussion: university designal-time VGA 3-D image sensor using mixed-signal techniques
Yusuke Oike, Makoto Ikeda, Kunihiro Asada

January 2004 Proceedings of the 2004 conference on Asia South automation: electronic design and solution fair ASI of the 2004 conference on Asia South Pacific design and solution fair ASP-DAC '04

Publisher: IEEE Press

Full text available: 4 pdf(506.41

KB) 🗐 Publisher

Additional Information: <u>full citation</u>,

Site

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6 SPOTS'06 session 4--new sensors and architectures: The low power

processing (LEAP)embedded networked sensor system

Dustin McIntire, Kei Ho, Bernie Yip, Amarjeet Singh, Winston Wu, William

April 2006 Proceedings of the fifth international conference on In sensor networks IPSN '06

Publisher: ACM Press

Full text available: pdf(200.80 Additional Information: full citation, KB) index terms

A broad range of embedded networked sensor (ENS) systems for critica applications now require complex, high peak power dissipating sensor d demand high performance computing and high bandwidth communication demands for these new platforms include support for computationally ir processing as well as optimization and statistical computing. To meet the while maintaining critical support for ...

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platform hardware and software architecture

7 Applications: Cyclops: in situ image sensing and interpretation in wil

Mohammad Rahimi, Rick Baer, Obimdinachi I. Iroezi, Juan C. Garcia, Jay \
Mani Srivastava

November 2005 Proceedings of the 3rd international conference networked sensor systems SenSys '05

Publisher: ACM Press

Full text available: pdf(1.25 Additional Information: full citation, MB)

MB)

Additional Information: full citation, citings, inde

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Paul Debevec, Erik Reinhard, Greg Ward, Sumanta Pattanaik
August 2004 ACM SIGGRAPH 2004 Course Notes SIGGRAPH '04

Publisher: ACM Press

Full text available: pdf(20.22 MB)

Additional Information: full citation,

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9 Suffix arrays: what are they good for?

Simon J. Puglisi, William F. Smyth, Andrew Turpin

January 2006 Proceedings of the 17th Australasian Database Cor

Publisher: Australian Computer Society, Inc.

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February 2003 Proceedings of the 2003 ACM/SIGDA eleventh int on Field programmable gate arrays FPGA '03

Publisher: ACM Press

Full text available: pdf(187.05 Additional Information: full citation, KB)

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12 Considerations in processing satellite images
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http://portal.acm.org/results.cfm?CFID=37362561&CFTOKEN=65698102&adv=1&COLL=... 1/18/07

Publisher: ACM Press

Full text available: 2 pdf(547.79

Additional Information: full citation,

KB)

Legislated demands for better control of natural resources have motivat resource management agencies to investigate application of satellite im management activities. The University of Santa Clara and NASA-Ames F jointly considering the problems faced by such agencies. Advantages an relatively small systems for the required computer processing activities paper. In addition, summar ...

13 VLSI circuits: Design of a nanosensor array architecture

🚗 Wei Xu, N. Vijaykrishnan, Y. Xie, M. J. Irwin

April 2004 Proceedings of the 14th ACM Great Lakes symposium (

Publisher: ACM Press

Full text available: pdf(1.37 Additional Information: full citation, MB) index terms

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Keywords: electronic nose, gas sensing, nanowire sensor array, patter processing

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Paul E. Debevec, Jitendra Malik

August 1997 Proceedings of the 24th annual conference on Compinteractive techniques SIGGRAPH '97

Publisher: ACM Press/Addison-Wesley Publishing Co.

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15 Towards design and validation of mixed-technology SOCs

S. Mir, B. Charlot, G. Nicolescu, P. Coste, F. Parrain, N. Zergainoh, B. Cou March 2000 Proceedings of the 10th Great Lakes symposium on \ **Publisher: ACM Press**

Full text available: 2 pdf(581.54 Additional Information: full citation, index terms

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Karlheinz Weiß, Thorsten Steckstor, Gernot Koch, Wolfgang Rosenstiel February 1999 Proceedings of the 1999 ACM/SIGDA seventh inte Field programmable gate arrays FPGA '99

Publisher: ACM Press

Full text available: pdf(2.02 Additional Information: full citation, MB)

- 17 Compilation: Automated compile-time and run-time techniques to in-
- MMU-less embedded systems

Lan S. Bai, Lei Yang, Robert P. Dick

October 2006 Proceedings of the 2006 international conference of architecture and synthesis for embedded systems

Publisher: ACM Press

Full text available: 2 pdf(1.94 Additional Information: full citation. MB) index terms

Random access memory (RAM) is tightly-constrained in many embedde especially true for the least expensive, lowest-power embedded system nodes and portable consumer electronics. The most widely-used sensor 4-10 KB of RAM and do not contain memory management units (MMUs) implement increasingly complex applications under such tight memory (price and power consumption constraints make ...

Keywords: data compression, embedded system, wireless sensor netw

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19 A prototype VLSI chip architecture for JPEG image compression

M. Kovac, N. Ranganathan, M. Zagar

March 1995 Proceedings of the 1995 European conference on De

Publisher: IEEE Computer Society

Full text available: pdf(542.19

KB) 🗐 Publisher

Additional Information: full citation,

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In this paper, we describe the design and implementation of a prototype architecture for implementing the JPEG baseline image compression stathe principles of pipelining and parallelism to the maximum extent in or and throughput. The architecture for discrete cosine transform and the on efficient algorithms designed for high speed VLSI implementation. Thusing the Cadence tools and b ...

Keywords: 100 MHz, 1024 pixel, 1048576 pixel, CMOS digital integrate DCT, DSP chip, JAGUAR architecture, JPEG baseline image compression compression, VLSI, VLSI chip architecture, color images, data compress processing chips, discrete cosine transform, discrete cosine transforms, encoder, high speed IC, high throughput, image coding, parallel archite processing, pipelining, prototype implementation

20 SPOTS track: Networked infomechanical systems: a mobile embedo platform

Richard Pon, Maxim A. Batalin, Jason Gordon, Aman Kansal, Duo Liu, Moh Shirachi, Yan Yu, Mark Hansen, William J. Kaiser, Mani Srivastava, Gaurav April 2005 Proceedings of the 4th international symposium on Inference sensor networks IPSN '05

Publisher: IEEE Press

Full text available: pdf(365.69 KB) Additional Information: full citation,

Networked Infomechanical Systems (NIMS) introduces a new actuation networked sensing. By exploiting a constrained actuation method based infrastructure, NIMS suspends a network of wireless mobile and fixed sed dimensional space. This permits run-time adaptation with variable sensition and even sensor type. Discoveries in NIMS environmental investigations for 1) new embedded platforms int ...

Keywords: actuation, embedded, mobility, networked, sensor, system

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20 SPOTS track: Networked infomechanical systems: a mobile embeding platform

Richard Pon, Maxim A. Batalin, Jason Gordon, Aman Kansal, Duo Liu, Moh Shirachi, Yan Yu, Mark Hansen, William J. Kaiser, Mani Srivastava, Gaurav April 2005 Proceedings of the 4th international symposium on Inference sensor networks IPSN '05

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1 Layout tools for analog ICs and mixed-signal SoCs: a survey

Rob A. Rutenbar, John M. Cohn

May 2000 Proceedings of the 2000 international symposium on P

Publisher: ACM Press

Full text available: Dpdf(247.03 Additional Information: full citation,

KB)

2 (Special session) presentation + poster disscussion: university designated and provided the session of the se real-time VGA 3-D image sensor using mixed-signal techniques Yusuke Oike, Makoto Ikeda, Kunihiro Asada

January 2004 Proceedings of the 2004 conference on Asia South automation: electronic design and solution fair ASI of the 2004 conference on Asia South Pacific design design and solution fair ASP-DAC '04

Publisher: IEEE Press

Full text available: pdf(506.41

KB) 🗐 Publisher

Additional Information: full citation,

Site

We have developed the first real-time 3-D image sensor with VGA pixel signal techniques to achieve high-speed and high-accuracy range calcul section method. Our mixed-signal position detector, which consists of a and time-domain approximate ADCs, provides significant information fo during high-speed analog-to-digital conversion. Moreover the position a profile of a projected be ...

3 "Empty space" computes: the evolution of an unconventional superce Jonathan W. Mills, Matt Parker, Bryce Himebaugh, Craig Shue, Brian Kope May 2006 Proceedings of the 3rd conference on Computing fronti

Publisher: ACM Press

Full text available: pdf(1.82 Additional Information: full citation, index terms

Lee A. Rubel defined the extended analog computer to avoid the limitation purpose analog computer. Partial differential equation solvers were a "q Rubel's theoretical machine. These components have been implemented VLSI circuits without transistors, as well as conductive plastic. For the p Indiana University has explored the design and applications of extended machines have become incre ...

Keywords: Lukasiewicz logic, extended analog computer, general purp hybrid digital-analog architecture

4 Personal imaging and lookpainting as tools for personal documental photojournalism

Steve Mann

March 1999 Mobile Networks and Applications, Volume 4 Issue 1

Publisher: Kluwer Academic Publishers

Full text available: pdf(2.24 Additional Information: full citation, MB) citings, inde

A means and apparatus for covert capture of extremely high-resolution presented. The apparatus embodies a new form of user-interface – instant click" metaphor which was thought to be the simplest photography proposed is a "look" metaphor in which images are generated through t looking around, in a manner that does not require conscious thought or

5 A 3-pin 1.5 V 550 mW 176 x 144 self-clocked CMOS active pixel im

Kwang-Bo Cho, Alexander Krymski, Eric Fossum

August 2001 Proceedings of the 2001 international symposium o and design ISLPED '01

Publisher: ACM Press

Full text available: pdf(350.69 Additional Information: full citation, KB)

Keywords: CMOS, active pixel sensor, image sensor, low-power, low-v

6 Reviewed papers: Using image processing to teach CS1 and CS2

Kenny Hunt

December 2003 ACM SIGCSE Bulletin, Volume 35 Issue 4

Publisher: ACM Press

Full text available: pdf(676.87 Additional Information: full citation, KB) citings

The use of digital image processing techniques in undergraduate computation advantages in terms of motivating student interest and immediate, visu code. Although the standard Java distribution includes support for basic operations, including the display of images, the complexity of the packatinexperienced programmers. This paper presents an extension to the but package that is suitable f ...

7 High performance imaging using large camera arrays

Bennett Wilburn, Neel Joshi, Vaibhav Vaish, Eino-Ville Talvala, Emilio Antu Adams, Mark Horowitz, Marc Levoy

July 2005 ACM Transactions on Graphics (TOG), ACM SIGGRAPH '05, Volume 24 Issue 3

Publisher: ACM Press

Full text available: pdf(902.47 KB) mov Additional Information: full citation, citings, inde

The advent of inexpensive digital image sensors and the ability to create information from a number of sensed images are changing the way we. In this paper, we describe a unique array of 100 custom video cameras summarize our experiences using this array in a range of imaging applic explore the capabilities of a system that would be inexpensive to product mind, we used s ...

Keywords: camera arrays, spatiotemporal sampling, synthetic aperture

8 High dynamic range imaging

Paul Debevec, Erik Reinhard, Greg Ward, Sumanta Pattanaik

August 2004 ACM SIGGRAPH 2004 Course Notes SIGGRAPH '04

Publisher: ACM Press

Full text available: 2 pdf(20.22

Additional Information: full citation

Current display devices can display only a limited range of contrast and main reasons that most image acquisition, processing, and display techneight bits per color channel. This course outlines recent advances in hig from capture to display, that remove this restriction, thereby enabling it color gamut and dynamic range of the original scene rather than the lim current monitor ...

9 Scanned-display computer graphics

A. Michael Noll

March 1971 Communications of the ACM, Volume 14 Issue 3

Publisher: ACM Press

A television-like scanned-display system has been successfully impleme 224 computer installation. The scanned image is stored in the core men software scan conversion is used to convert the rectangular coordinates appropriate word and bit in an output display array in core storage. Res flicker-free displays of large amounts of data are possible with reasonat interaction. A scanned im ...

Keywords: computer graphics, raster displays, scan conversion, scann

10 Applications: Cyclops: in situ image sensing and interpretation in wil

Mohammad Rahimi, Rick Baer, Obimdinachi I. Iroezi, Juan C. Garcia, Jay Mani Srivastava

November 2005 Proceedings of the 3rd international conference networked sensor systems SenSys '05

Publisher: ACM Press

Full text available: pdf(1.25 Additional Information: full citation, oitings, inde

Despite their increasing sophistication, wireless sensor networks still do

powerful of the human senses: vision. Indeed, vision provides humans to distinguish objects and identify their importance. Our work seeks to painth similar capabilities by exploiting emerging, cheap, low-power and simaging technology. In fact, we can go beyond the stereo capabilities of the large scale of ...

Keywords: CMOS imaging, imaging, power efficiency, sensor network,

11 Progress in Picture Processing: 1969--71

Azriel Rosenfeld

June 1973 ACM Computing Surveys (CSUR), Volume 5 Issue 2

Publisher: ACM Press

Full text available: pdf(2.34 Additional Information: full citation.

MB) terms

12 An on-line image processing system

I. H. Barkdoll, B. L. McGlamery

January 1968 Proceedings of the 1968 23rd ACM national conference.

Publisher: ACM Press

Full text available: pdf(2.04

Additional Information: full citation,

MB)

The high-speed digital computer has contributed to significant progress particular area of optics benefiting from this progress is image processing processing is to aid the human observer in extracting from an image inf obscured by some type of degradation The numerous factors which can image in an optical system include lens aberrations, poor focus, image if it is the process of the computer of the progress of the computer of

13 MAPS: a generalized image processor

Michael Fischer

September 1973 ACM SIGGRAPH Computer Graphics, Volume 7 Is:

Publisher: ACM Press

Full text available: pdf(769.35 Additional Information: full citation,

By approaching two and three-dimensional problems from the spatial vi geographic sciences, the <u>M</u>ulti-dimensional <u>A</u>nalysis

<u>S</u>ystem (MAPS) is able to achieve high efficiency and large cal interactive graphics, simulation modeling, and image processing. Spatia presented by means of color images, rather than line drawings, facilitat

- 14 Track 6: autonomic and organic computing: Marching-pixels: a new
- paradigm for smart sensor processor arrays

Dietmar Fey, Daniel Schmidt

May 2005 Proceedings of the 2nd conference on Computing front Publisher: ACM Press

Full text available: pdf(606.57 Additional Information: full citation, KB) index terms

In this paper we present a new organic computing principle denoted as architectures of future smart CMOS camera chips. The idea of marching realization of a massively-parallel fine-grain single-chip processor array organic units which are propagating in a pixel processor array, similar to algorithms. The task of the marching pixels is to carry out autonomously processing tasks, e.g...

Keywords: image pre-processing, organic computing, self-organization smart pixels

15 Inexpensive real-time image generation and control

Bill Etra, Lou Katz

April 1977 ACM SIGGRAPH Computer Graphics, Volume 11 Issue 1

Publisher: ACM Press

Full text available: pdf(603.12 KB) Additional Information: full citation,

16 <u>TPphotoSuite: a windows based digital image processing program</u>
Tauhida Parveen

January 2004 Journal of Computing Sciences in Colleges, Volume Publisher: Consortium for Computing Sciences in Colleges Full text available: pdf(184.78 Additional Information: full citation, KB)

The purpose of this paper is to present a Windows based software tool r capable of performing image-processing operations. *TPphotoSuite* is fre

compatible platform, the existing image processing operations can be m operations can be added to it. TPphotoSuite provides a user-friendly GU computer literacy for it to use. It contains many features that are used ~as, colo ...

17 Three-dimensional medical imaging: algorithms and computer system

M. R. Stytz, G. Frieder, O. Frieder

December 1991 ACM Computing Surveys (CSUR), Volume 23 Issue

Publisher: ACM Press

Full text available: pdf(7.38 MB)

Additional Information: full citation.

terms, review

Keywords: Computer graphics, medical imaging, surface rendering, th volume rendering

18 Cellular wave computers and CNN technology - a SoC architecture sensor arrays

T. Roska

May 2005 Proceedings of the 2005 IEEE/ACM International confe aided design ICCAD '05

Publisher: IEEE Computer Society

Full text available: pdf(415.00 Additional Information: full citation,

Cellular wave computers and cellular nonlinear network (CNN) technological paper. It is a system-on-chip (SoC) architecture with xK processors and architectural lessons from the trends in manufacturing billion componen the threshold of 100 nm feature size will determine the architecture, the and the type of algorithms needed, hence also the complexity of the sol

19 Anti-aliasing in topological color spaces

Kenneth Turkowski

August 1986 ACM SIGGRAPH Computer Graphics, Proceedings of conference on Computer graphics and interactive te '86, Volume 20 Issue 4

Publisher: ACM Press

Full text available: pdf(5.19 Additional Information: full citation,

MB)

index terms

The power of a color space to perform well in interpolation problems sur smooth-shading is dependent on the topology of the color space as well it contains. We develop the *Major-minor* color space, which has a topological lends itself to simple anti-aliasing computations between elements of ar an inexpensive frame store.

20 Bio-Inspired Analog VLSI Design Realizes Programmable Complex Dynamics on a Single Chip

R. Carmona, F. Jiménez-Garrido, R. Domínguez-Castro, S. Espejo, A. Rodí March 2002 Proceedings of the conference on Design, automation DATE '02

Publisher: IEEE Computer Society

Full text available: 4 pdf(4.22

MB)

Additional Information: full citation,

A bio-inspired model for an analog parallel array pro-cessor(APAP), base vertebrate retina, permits the realization of complex spatio-temporal dynamics the way in which images are processed in the visual pathway wh feasiblealternative for the implementation of early vision tasks instandal prototype chip has been designed in 0.5 µm CMOS. Design challenges, trablocks of such a high-complexity system (0.5 ...

Results 1 - 20 of 200

Result page: $1 \quad \underline{2} \quad \underline{3} \quad \underline{4} \quad \underline{5} \quad \underline{6} \quad \underline{7} \quad \underline{8}$

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